The Department of Electrical and Computer Engineering at Queen’s University requests applications from suitable qualified candidates interested in teaching the following undergraduate course in the 2018-19 academic session.

Winter Term Course: January 1, 2019 – April 30, 2019

Anticipated course enrolment: 100

Course Description

**ELEC 374**  
**Electrical and Computer Engineering Design**  
W 3-.1-2.5  4.25

High-performance logic design for arithmetic circuits; memory system designs based on static and dynamic RAMs; computer bus protocols and standard I/O interfaces; mass storage devices; hardware description languages (VHDL, Verilog); fault testing, design for testability, built-in self-test, memory testing, and boundary-scan architectures; asynchronous sequential circuit design; introduction to GPU architectures and GPU computing. The course is supplemented by a CPU design project that allows students to become proficient with Field Programmable Gate Array (FPGA) devices and associated CAD tools, as well as with GPU computing through nVidia CUDA or OpenCL languages.  

PREREQUISITE(S): ELEC 252, ELEC 271, ELEC 274 or permission of the instructor

Qualifications:

Minimum of a M.A.Sc. in Engineering or a related field, or a BASc in Engineering with extensive practical experience in engineering communications. Previous teaching experience at the University level will be preferred. Candidates should have excellent communication and presentation skills.

Course Syllabus can be found at: [http://www.ece.queensu.ca/Current-Students/Undergraduate/Undergraduate-Courses/LandingPages/ELEC-374/ELEC-374-CourseSummary.html](http://www.ece.queensu.ca/Current-Students/Undergraduate/Undergraduate-Courses/LandingPages/ELEC-374/ELEC-374-CourseSummary.html)

The above advertised course will be taught on campus. Winter term classes begin 07 January, 2019.

Queen’s University is committed to employment equity and diversity in the workplace and welcomes applications from women, visible minorities, aboriginal people, persons with disabilities, and persons of any sexual orientation or gender identity. All qualified candidates are encouraged to apply; however, Canadians and permanent residents of Canada will be given priority. Academic staff at Queen’s University is governed by a collective agreement between QUFA, [QUFA](http://www.queensu.ca) and Queen’s University.

The University will provide support in its recruitment processes to applicants with disabilities, including accommodation that takes into account an applicant’s accessibility needs. If you require accommodation during the interview process, please contact Mary Gillespie, mary.gillespie@queensu.ca

Applications should include a complete and current curriculum vitae, a statement of teaching experience, the names and contact details of two referees who may be contacted, and any relevant other materials the candidate wishes to submit for consideration. Applications can be submitted to the ECE Appointments.
Committee at the address below, or by email to Mary Gillespie at mary.gillespie@queensu.ca. Applications should be received no later than 12 October 2018.

Electrical and Computer Engineering Appointments Committee  
c/o Mary Gillespie, Administrative Assistant  
Department of Electrical and Computer Engineering  
Walter Light Hall, Room 416  
19 Union Street  
Queen’s University  
Kingston, ON K7L 3N6  
Tel.: 613-533-6000 Ext: 75344  
Fax: 613-533-6615